

- no major role. Due to the high sensitivity a microwave absorption signal caused by carrier emission from defects can be observed even in high-quality material at low injection levels. This allows for the first time the electrical investigation of the well-known thermal donor (TD) also in electronic grade p-doped silicon, which is not feasible with deep level transient
- spectroscopy (DLTS). Temperature treatment of such samples allows new insight into the transformation of TDs during annealing. Furthermore, the correlation with photoluminescence (PL) spectroscopy allows for an assignment of deep
- levels, which can be investigated by microwave-detected-photo-induced current transient spectroscopy (MD-PICTS)
 31 [Dornich K, Hahn T, Niklas JR. Freiberg Forschungsh B 2004; 327: 270; Dornich K, Hahn T, Niklas JR In: Proceedings of the MRS spring meeting vol. 864, 2005].
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1. Introduction

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Well-established electrical characterisation methods for semiconductors have several drawbacks.
However, some of them can be overcome by the
development of new contact- less spatially resolving
methods with high sensitivity like microwavedetected photoconductivity (MDP) and microwave-detected-photo-induced current transient

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53 spectroscopy (MD-PICTS) which are essential for analysing high-quality materials. This allows for 55 photoconductivity measurements at very low injection levels with so far not achieved sensitivity and 57 allows for a special evaluation of photoconductivity transients. Making use of a resonant microwave 59 detection system, the methods can be applied to a variety of semiconductor materials. This enables the 61 electrical characterisation of so far not detectable defects. Besides the possibility to furnish pieces of 63 information such as lifetime (τ) , diffusion length (L)and mobility (μ) mappings by the so-called MDP, 65

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N	omenclature	τ	minority carrier lifetime (µs)	
3		μ	mobility (cm ² /Vs)	55
E_{λ}	activation energy (eV)	L	diffusion length (cm)	
5 λ	wavelength (nm)	Т	temperature (K and °C)	57
σ	resistivity (Ω cm)	Ω	capture cross section (cm^2)	6,

the techniques offer a new kind of defect spectro-9 scopy. Owing to the high sensitivity, at sufficiently low injection levels, thermal excitation of charge 11 carriers out of defect levels filled during a photo pulse can be observed, see Fig. 1. After the light is 13 turned off the photoconductivity transient signal consists mainly of two parts. The first part, a fast 15 decay, corresponds to the minority carrier lifetime. This is followed by a much slower decay process due 17 to the thermal emission of carriers out of defect levels. Important is the choice of sufficiently long 19



Fig. 1. Typical photoconductivity signal for a rectangular light pulse, p-silicon wafer.

photo-pulses in order to fill defect levels. Typical 61 photo-pulse lengths are around 0.1-1 ms. The time constant of the slow emission depends on the defect 63 activation energy, the defect capture cross section for electrons or holes, and on the temperature. 65 Temperature-dependent measurements of such signals lead to defect specific photo-conductivity 67 transients which can be used in a similar way as DLTS capacitance transients to gain specific in-69 formation about the defects under investigation. This opens the possibility to obtain defect spectra as 71 with DLTS measurements: however, contact-less, non-destructive, and highly spatially resolved [1]. 73 Moreover, doping is not a critical parameter and the investigations are not restricted to just deep levels. 75 We termed this kind of experiment MD-PICTS [2,3]. In contrast to DLTS, with MD-PICTS defects 77 are filled by carriers via the valence or conduction band, or both. Therefore, MD-PICTS spectra on 79 the one hand give access to a variety of defects so far not detectable by other electrical characterisation 81 methods. On the other hand, some defects seen by DLTS are invisible by MD-PICTS. Depending on 83 the concentration of the defects, the sensitivity of the detection system must be extremely high in 85 order to see the defect specific slow part of the transient signal at all. At the same time, the light 87 intensity must be even kept extremely low sometimes in order to prevent the photoconductivity 89



Fig. 2. Scheme of the experimental setup.

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- 1 transient from being completely dominated by just the otherwise big part due to the free carrier
- 3 lifetime. Just one example is taken from high quality 6" electronic grade p-doped silicon wafers with
- 5 specific resistivities around $12 \Omega cm$. The experimental setup is depicted in Fig. 2.
- 7 Absorption, not reflection, of the microwave field is used to measure the conductivity of the sample. The
- 9 wafer is part of a special microwave system. Key feature is the appropriate coupling of the sample to
- 11 a resonant microwave cavity. Due to the variety of electrical parameters accessible and high sensitivity
- 13 there are now drawbacks in comparison with microwave reflection methods. For mappings the
- 15 transients are recorded while the wafer is moving. In general, the wafer can be of unlimited size. For
- 17 temperature-dependent measurements, such as MD-PICTS, the wafer must be part of a cryogenic
- 19 system, which provides a further experimental challenge. The injection levels used correspond to
- 21 an optical power of the light source in the range of about $10\,\mu W$ to $10\,m W$ depending on the defect

23 concentration and type of semiconductor.

25 2. Results and discussion

- 27 Following the defect evolution during thermal treatment of p-doped silicon by MD-PICTS allows
- 29 for the observation of two defect levels. Fig. 3a shows MD-PICTS spectra and Fig. 3b correspond-
- 31 ing PL spectra of two temperature treatment steps. As grown samples and samples treated for 40 min at
- 33 450 °C show a defect at low temperatures in the MD-PICTS spectra, which we called PTD. This
 35 peak is believed to be due to the well known thermal
- donors (TDs) in silicon, which cannot be observed 37 by DLTS in p-material because of the position of
- the Fermi level. The emission maxima are in this
- 39 case at 133 K in as-grown and at 140 K in the 450 °C treated sample. After a 650 °C temperature step, the
- 41 emission maximum shifts more than 40° to lower temperatures and is now located at 96K. The
- 43 activation energy shifts only slightly from $E_A = 0.13 \text{ eV}$ in the as- grown sample to $E_A = 0.11 \text{ eV}$ in
- 45 the 650 °C treated sample. The temperature shift is due to a change in capture cross section, from $\sigma =$
- 47 $2 \times 10^{-18} \text{ cm}^2$ in as-grown material to $\sigma = 2 \times 10^{-15} \text{ cm}^2$ in 650 °C treated samples. The 49 destruction of TDs at temperatures above 600 °C
- is well known in literature [4]. However, the MD-
- 51 PICTS results suggest the transformation of such donors to electrically inactive trap states.



Fig. 3. a. MD-PICTS spectra of temperature treated electronic grade p-silicon: IR-LED $\lambda = 950$ nm, optical output $130 \,\mu$ W. b. PL spectra of temperature treated electronic grade p-silicon: $\lambda = 850$ nm, T = 1.6 K.

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A second defect level with $E_A = 0.26 \text{ eV}$ in asgrown samples can be observed at 242 K. During temperature treatment, a rise in concentration and a shift of activation energy to around $E_A = 0.35 \text{ eV}$ can be seen. There is no information on the nature of this defect level called PD. This information may be obtained by further temperature treatment and correlation with PL spectra.

Three additional temperature steps at 750 °C, 93 900 °C and rapid thermal annealing (RTA) at 740 °C have been studied (Fig. 4a). There is a 95 constant temperature shift of the PTD peak down to 78 K for the 900 °C temperature step accompa-97 nied by a drastic drop in concentration. This is believed to be due to the nearly complete destruc-99 tion of the TD trap states at higher temperatures. The RTA step shows in MD-PICTS spectra only a 101 drop in intensity in comparison to the 750 °C and 650 °C temperature steps. The PD defect shows a 103 rise in concentration for the highest temperatures.

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Fig. 4. a. MD-PICTS spectra of temperature treated electronic grade p-silicon at higher temperatures: IR-LED $\lambda = 950$ nm, optical output 130 µW. b. PL spectra of temperature treated electronic grade p-silicon, $\lambda = 850$ nm, T = 1.6 K.

PL spectra for the same samples show besides 53 several well known defect levels an increase in concentration for the D1 and D2 peaks at around 55 0.81 and 0.87 eV (Fig. 4b). Both defects are due to dislocations and kinks, jogs and stacking faults in 57



Fig. 5. PL peak intensity as a function of temperature treatment. 83





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- 1 the vicinity of dislocations. The peak intensity in dependence of temperature treatment is shown in
- 3 Fig. 5. Among others, the D4- and the I-line exhibit no intensity change with temperature treatment.
- 5 However, the D1 and D2 lines increase their peak intensities with higher annealing temperatures. It is
- 7 therefore suggested that the PD defect level in the MD-PICTS spectra is identical with the D1 and D2
- 9 peaks in the PL spectra.Besides mapping of a variety of electronic grade
- 11 and solar grade silicon wafers contact- less and fast electrical characterisation of epitaxial layers was the
- 13 greatest challenge for the new method. Measurements are easily feasible down to an epi- layer
- 15 thickness of about $2\,\mu m$, even on very high conductivity substrates. Doping levels are not
- 17 critical down to below 0.1Ω cm. Just one example of the mapping capability by MDP for an epi- layer
- 19 is shown in Fig. 6a. A diffusion length map shows clearly the inhomogeneities within the layer. An
- 21 absolute value of the diffusion length cannot be extracted without calibration. However the integral
- 23 over the area underneath the photopulse is proportional to the square of the lifetime and such
- 25 differences can be mapped. Under certain circumstances further information can be extracted by the
- 27 variation of the excitation wavelength. Lifetime values differ by about a factor of 2.5, typical
- 29 transients are shown in Fig. 6b. The investigation of the same layer at a higher wavelength of the
- 31 exciting laser furnishes also information on interface defects on the backside of the epi- layer [3].

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3. Conclusion

MDP and MD-PICTS are new contact- less, nondestructive methods for the characterisation of a

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variety of semiconductors [5]. The identification of two peaks measured with MD-PICTS in electronic 41 grade p-silicon was accomplished. One of them is due to the family of TD defects. Temperature 43 treatment of such samples results in a significant change in the position of the TD peak. This is 45 believed to be due to the transformation of an electrically active TD state to an electrically inactive 47 trap state at temperatures above 600 °C. At higher temperatures, the concentration of this defect peak 49 drops rapidly suggesting the destruction of this defect level at temperatures above 900 °C. A second 51 peak could be correlated with PL measurements and is assigned to defect levels in the vicinity of 53 dislocations. MDP is as well suited for the electrical characterisation of a variety of epitaxial layers. 55

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